Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOS VII)

TK13A60D

Switching Regulator Applications

- Low drain-source ON-resistance: RDS (ON) = 0.33Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 6.5 \text{ S (typ.)}$

Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 600 \text{ V)}$ Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$ Absolute Maximum Ratings (Ta = 25°C) 0.69 ± 0.15 **♦ Ø** 0.2**M** A Characteristics Symbol Rating Unit Drain-source voltage 600 V_{DSS} V Gate-source voltage ±30 V_{GSS} 43 (Note 1) I_D Drain current 1: Gate Pulse (Note 1) I_{DP} 52 2: Drain 3: Source Drain power dissipation ($Tc = 25^{\circ}C$) P_D 50 W Single pulse avalanche energy 511 ΜJ EAS , (Note 2) **JEDEC** Avalanche current IAR 13 A **JEITA** SC-67 Repetitive avalanche energy (Note 3) EAR 5.0 mJ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

150

-55 to 150

°C

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Thermal Characteristics

Channel temperature

Storage temperature range

	< 11		
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	2.5	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

 T_{ch}

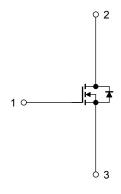
Tstg

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 5.3 mH, $R_G = 25 \Omega$, $I_{AR} = 13 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



2-10U1B

TOSHIBA

Weight: 1.7 g (typ.)

Start of commercial production 2008-07

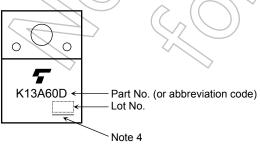
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off curr	ent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	10	μА
Drain-source bre	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_		٧
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	٧
Drain-source ON	-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 6.5 A	(F	0.33	0.43	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6.5 A	1.8	6.5		S
Input capacitance	е	C _{iss}		()	2300		
Reverse transfer	capacitance	C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		10		pF
Output capacitan	ice	Coss		⁷ —	250		
Switching time	Rise time	t _r	10 V I _D = 6.5 A V _{OUT}	_	50	<i> </i>	
	Turn-on time	t _{on}	$\begin{array}{c c} 0 \text{ V} & & \\ \hline 50 \Omega & & \\ \end{array} \begin{array}{c} R_L = 30 \Omega \end{array}$	-6	100	> —	ns
	Fall time	t _f	/// // // // // // // // // // // // //	A	25) _	115
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs	(A)	140		
Total gate charge	е	Qg			40	_	
Gate-source cha	rge	Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$) —	25	_	nC
Gate-drain charg	е	Q _{gd}		_	15	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		_	_	13	Α
Pulse drain reverse current (Note 1)	I _{DRP}	(7/\\ -	_	_	52	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = 13 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	trr	I _{DR} = 13 A, V _{GS} = 0 V,	_	1600	_	ns
Reverse recovery charge	Qrr	$dI_{DR}/dt = 100 A/\mu s$		20		μС

Marking

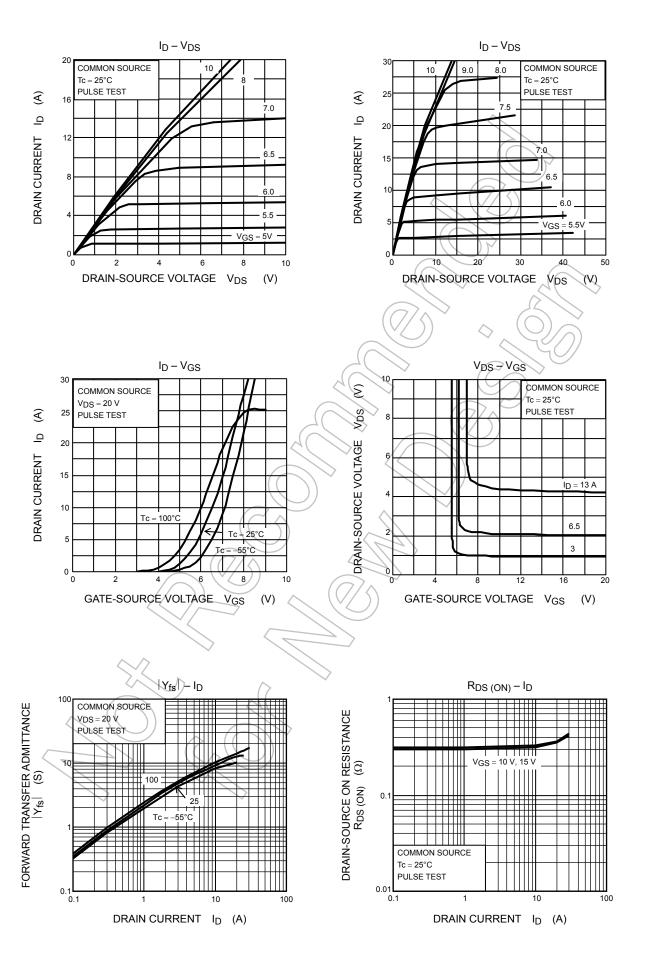


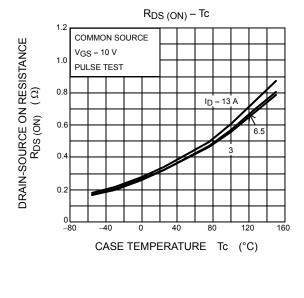
Note 4: A line under a Lot No. identifies the indication of product Labels.

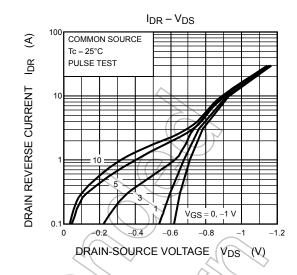
Not underlined: [[Pb]]/INCLUDES > MCV

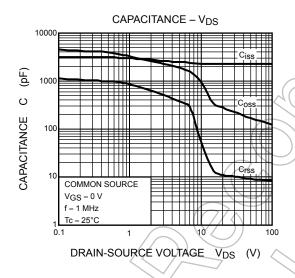
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

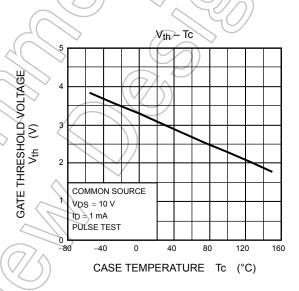
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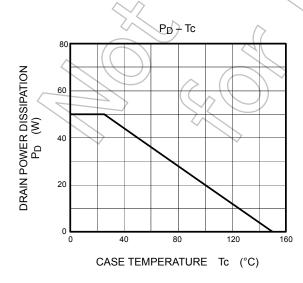


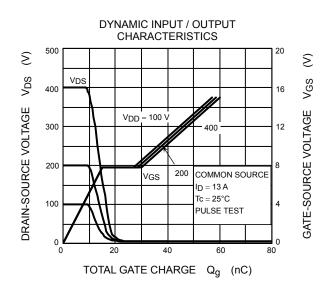


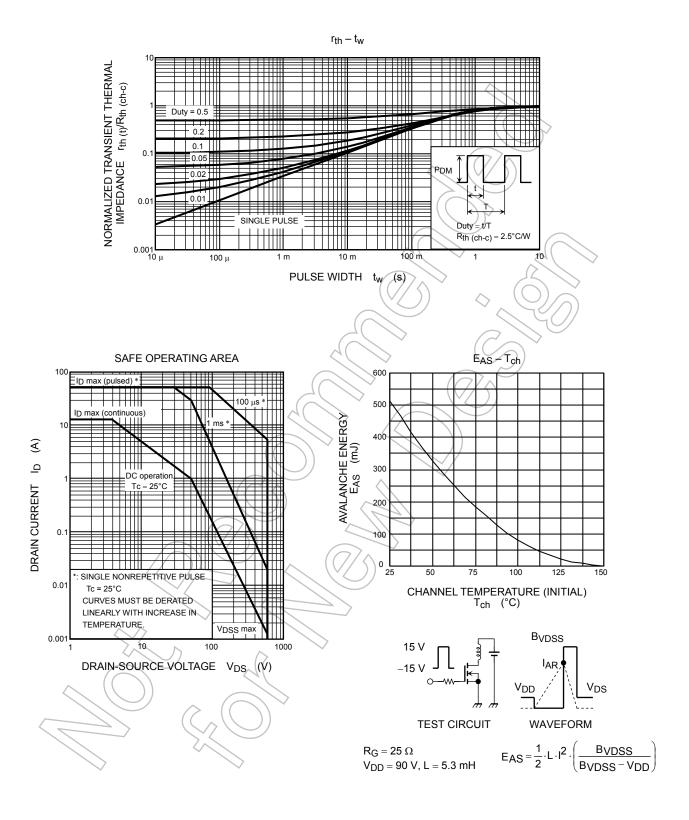












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