NPN Triple Diffused Planar Silicon Transistor



2SC3039

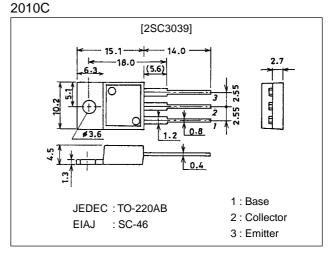
# 400V/7A Switching Regulator Applications

## Features

- · High breakdown voltage ( $V_{CBO} \ge 500V$ ).
- · Fast switching speed.
- $\cdot$  Wide ASO.

# **Package Dimensions**

unit:mm



# **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		500	V
Collector-to-Emitter Voltage	VCEO		400	V
Emitter-to-Base Voltage	V <sub>EBO</sub>		7	V
Collector Current	۱ <sub>C</sub>		7	А
Collector Current (Pulse)	ICP	PW≤300µs, Duty Cycle≤10%	14	А
Base Current	Ι <sub>Β</sub>		3	Α
Collector Dissipation	PC		1.75	W
		Tc=25°C	50	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V <sub>CB</sub> =400V, I <sub>E</sub> =0			10	μA
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =5V, I <sub>C</sub> =0			10	μΑ
DC Current Gain	h <sub>FE</sub> 1	$V_{CE}=5V, I_{C}=0.8A$	15*		50*	
De current Gain	h <sub>FE</sub> 2	V <sub>CE</sub> =5V, I <sub>C</sub> =4A	8			
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =4A, I <sub>B</sub> =0.8A			1.0	V
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =4A, I <sub>B</sub> =0.8A			1.5	V

\*: The h<sub>FE</sub>l of the 2SC3039 is classified as follows. When specifying the h<sub>FE</sub>l rank, specify two ranks or more in principle.

15 L 30 20 M 40 30 N 50

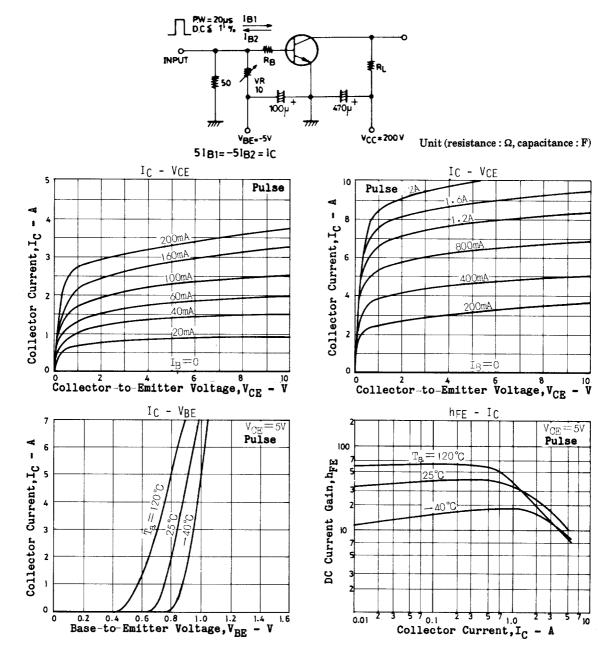
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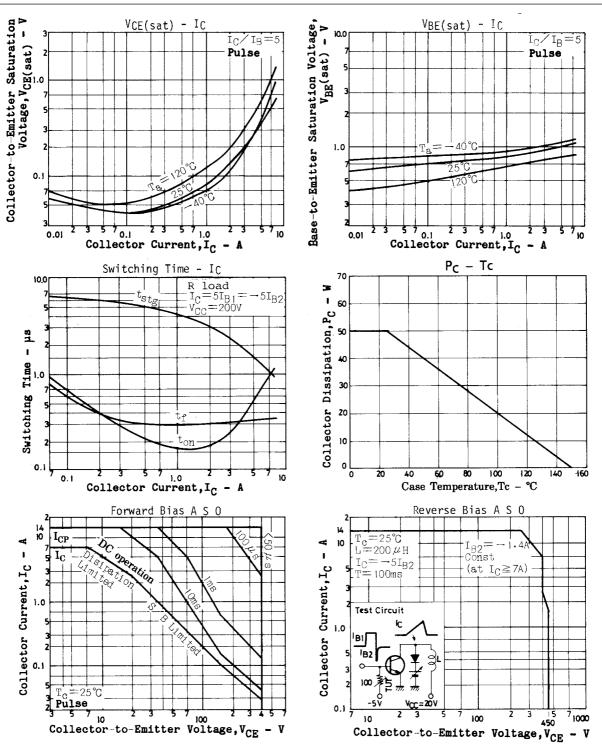
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Gain-Bandwidth Product	fT	V <sub>CE</sub> =10V, I <sub>C</sub> =0.8A		20		MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, f=1MHz		80		pF
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =1mA, I <sub>E</sub> =0	500			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =5mA, R <sub>BE</sub> =∞	400			V
Emitter-to-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> =1mA, I <sub>C</sub> =0	7			V
Collector-to-Emitter Sustain Voltage	V <sub>CEO(sus)</sub>	I <sub>C</sub> =7A, I <sub>B</sub> =1.4A, L=50µH	400			V
Collector-to-Emitter Sustain Voltage	VCEX(sus)1	$I_{C}$ =7A, $I_{B1}$ =1.4A, L=200µH, $I_{B2}$ =-1.4A, clamped	400			V
	VCEX(sus)2	I <sub>C</sub> =1.5A, I <sub>B1</sub> =0.3A, L=200µH, I <sub>B2</sub> =–0.3A, clamped	450			V
Turn-ON Time	ton	I <sub>C</sub> =5A, I <sub>B1</sub> =1A, I <sub>B2</sub> =-1A, R <sub>L</sub> =40Ω, V <sub>CC</sub> =200V			1.0	μs
Storage Time	tstg	$I_{C}$ =5A, $I_{B1}$ =1A, $I_{B2}$ =-1A, $R_{L}$ =40 $\Omega$ , $V_{CC}$ =200V			2.5	μs
Fall Time	t <sub>f</sub>	$I_{C}=5A, I_{B1}=1A, I_{B2}=-1A, R_{L}=40\Omega, V_{CC}=200V$			1.0	μs

## **Switching Time Test Circuit**





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