TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

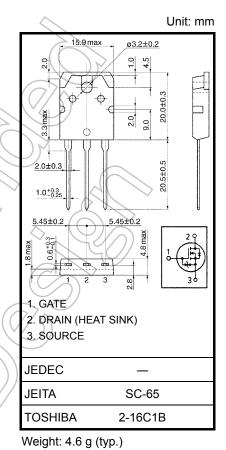
# 2SK2699

## Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance  $: RDS (ON) = 0.5 \Omega$  (typ.)
- High forward transfer admittance  $|Y_{fs}| = 11 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$
- Enhancement mode :  $V_{th} = 2.0$  to 4.0 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	600	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	600	V
Gate-source voltage		V <sub>GSS</sub>	±30	$\lor$ v
Drain current	DC (Note 1)	I <sub>D</sub>	(12)	А
	Pulse (Note 1)	I <sub>DP</sub>	48	A
Drain power dissipation	n (Tc = 25°C)	PD	150	////
Single pulse avalanche energy (Note 2)		EAS	605	Lm
Avalanche current		IAR	12	A
Repetitive avalanche energy (Note 3)			15	۳٦
Channel temperature		Tch	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	℃



 Storage temperature range
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 -55 to 150
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 Note:
 Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and

Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	0.833	°C / W
Thermal resistance, channel to ambient Rth (ch-a)	50	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 7.35 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

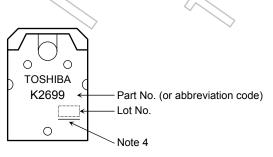
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	$V_{GS}$ = ±25 V, $V_{DS}$ = 0 V	_	—	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	X	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	1	-	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	)/	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A	77	0.5	0.65	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6 A	6.0	11.0	-	S
Input capacitance		C <sub>iss</sub>			2600	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	270	_	pF
Output capacitance		Coss		_	820	1	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{ov} \prod I \stackrel{I_{D}=6A}{}_{ov} V_{out}$	- (	45		
	Turn-on time	t <sub>on</sub>	$0V$ $L$ $R_L=50\Omega$	N	75	) —	
	Fall time	t <sub>f</sub>		$\sqrt{2}$	65	ns	ns
	Turn-off time	t <sub>off</sub>	$Duty \leq 1\%, t_{W} = 10 \mu s$	)-	270	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	58	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 480 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 12 \text{ A}$	_	37	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>		_	21	—	

### Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		_	_	12	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	<u> </u>	_	_	48	А
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_		-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	460	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 Å / μs	—	4.8	—	μC

#### Marking



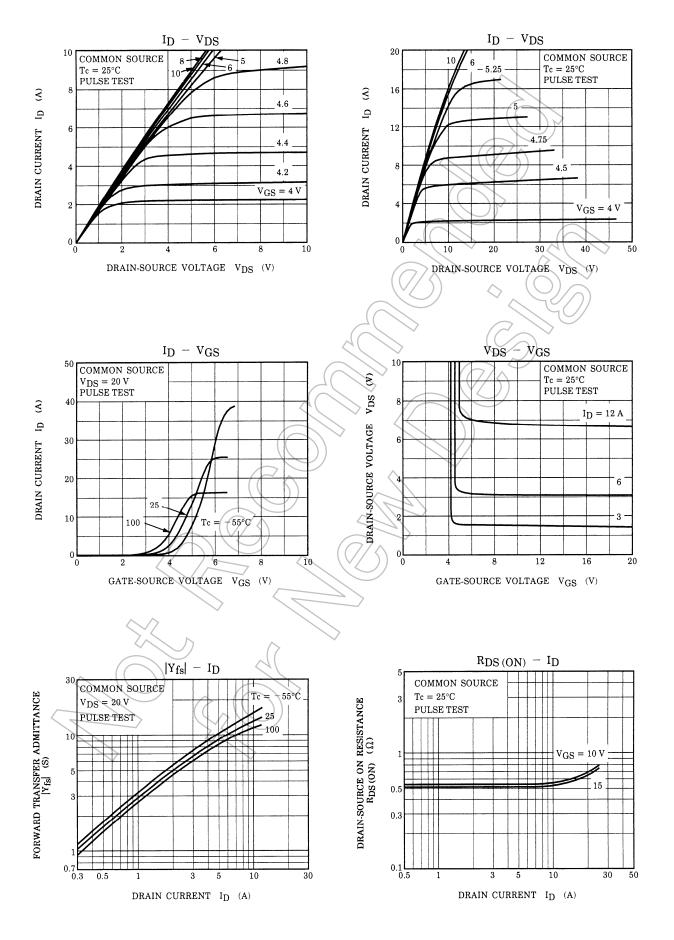
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

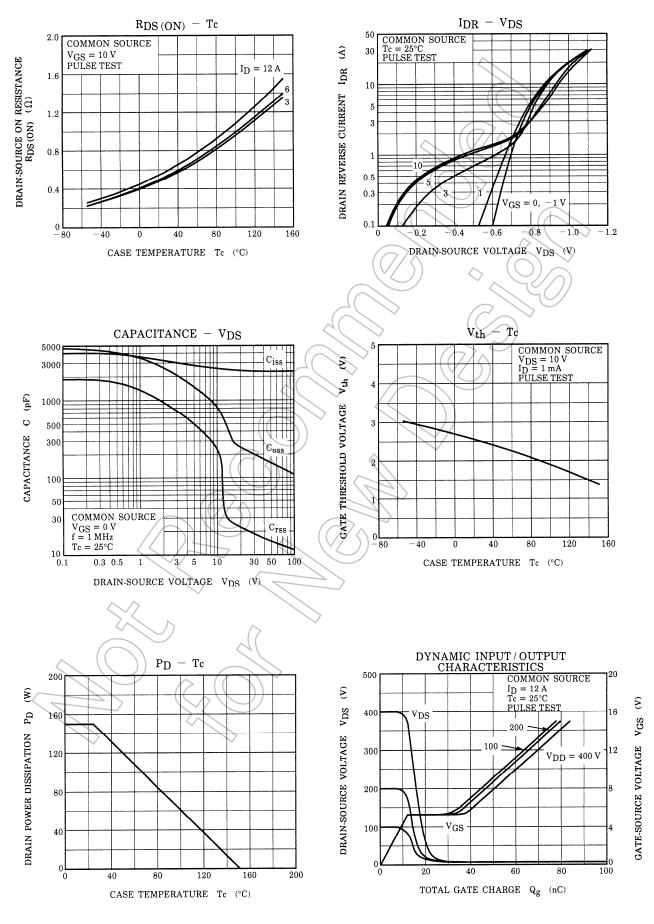
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

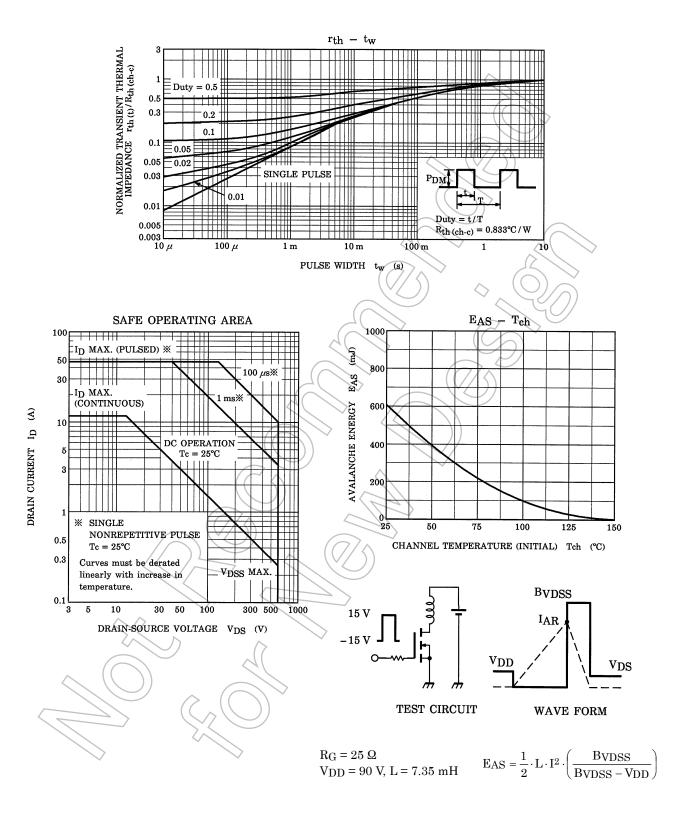
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