TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL MOS TYPE

GT40T101

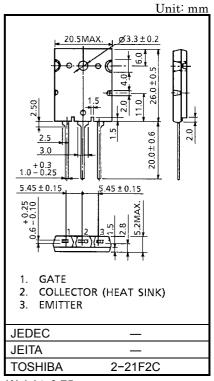
HIGH POWER SWITCHING APPLICATIONS

• Enhancement-Mode

 $\begin{array}{ll} \bullet & \mbox{High Speed} & : t_f = 0.4 \ \mu s \ (\mbox{Max.}) \ (\mbox{I}_C = 40 \ \mbox{A}) \\ \bullet & \mbox{Low Saturation} & : \mbox{V}_{CE} \ (\mbox{sat)} = 5.0 \ \mbox{V} \ (\mbox{Max.}) \ (\mbox{I}_C = 40 \ \mbox{A}) \\ \end{array}$

MAXIMUM RATINGS (Ta = 25°C)

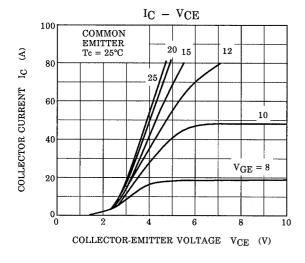
CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		V _{CES}	1500	V	
Gate-Emitter Voltage		V_{GES}	±25	V	
Collector Current	DC	IC	40	А	
	1ms	I _{CP}	80		
Collector Power Dissipation (Tc = 25°C)		P _C	200	W	
Junction Temperature		Tj	150	°C	
Storage Temperature Range		T _{stg}	-55~150	°C	

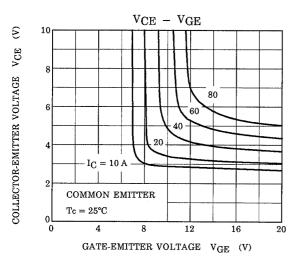


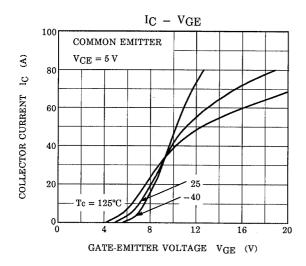
Weight: 9.75g

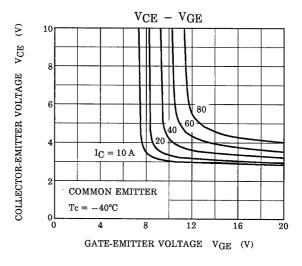
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

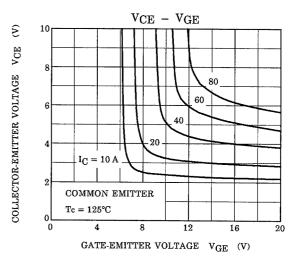
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Gate Leakage Curr	rent	I _{GES}	V _{GE} = ±25 V, V _{CE} = 0	_	_	±500	nA
Collector Cut-off C	urrent	I _{CES}	V _{CE} = 1500 V, V _{GE} = 0	_	_	1.0	mA
Gate-Emitter Cut-	off Voltage	V _{GE} (OFF)	I _C = 40 mA, V _{CE} = 5 V	3.0	_	6.0	V
Collector-Emitter S	Saturation Voltage	V _{CE} (sat)	I _C = 40 A, V _{GE} = 15 V	_	4.0	5.0	V
Input Capacitance		C _{ies}	V _{CE} = 10 V, V _{GE} = 0, f = 1 MHz	-	3600	_	pF
Switching Time	Rise Time	t _r	$ \begin{array}{c c} 15 \text{ V} & & & & & & \\ 0 & & & & & & \\ & & & & & \\ & & & & & \\ & & & & $	_	0.6	1.0	- µs
	Turn-On Time	t _{on}		_	0.7	1.1	
	Fall Time	t _f		_	0.2	0.4	
	Turn-Off Time	t _{off}		_	0.5	1.0	
Thermal Resistanc	e	R _{th (j-c)}	_	_	_	0.625	°C/W

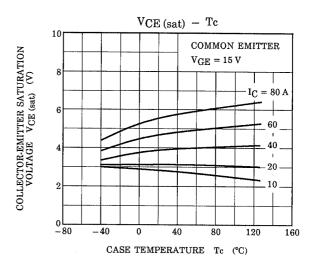


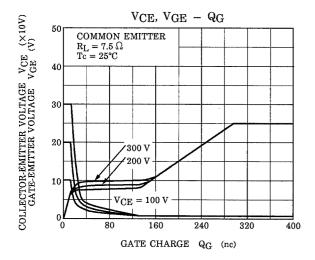


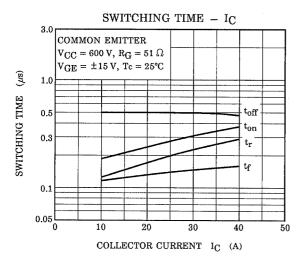


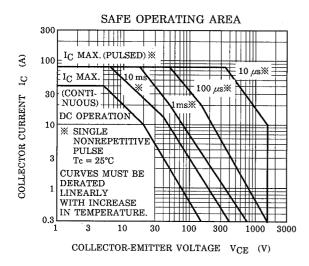


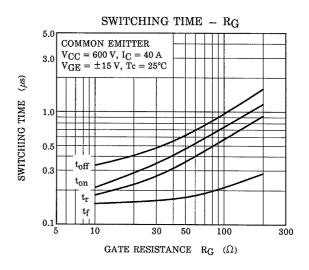


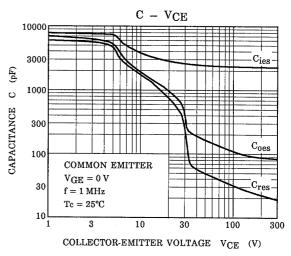


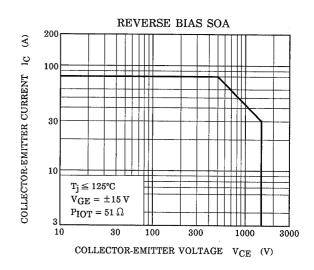






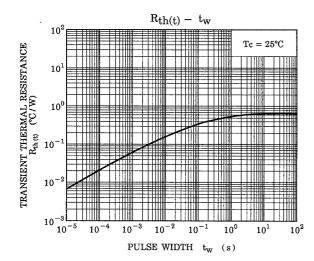






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