

# 6MBI100VA-060-50

**IGBT Modules** 

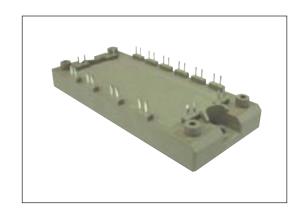
# IGBT MODULE (V series) 600V / 100A / 6 in one package

# **■** Features

Compact Package P.C.Board Mount Low Vce (sat)

## Applications

Inverter for Motor Drive
AC and DC Servo Drive Amplifier
Uninterruptible Power Supply
Industrial machines, such as welding machines



# ■ Maximum Ratings and Characteristics

# ● Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units	
	Collector-Emitter voltage		Vces			600	V
	Gate-Emitter v	te-Emitter voltage				±20	V
rter	Collector current		Ic	Continuous	Tc=80°C	100	
nvert			Icp	1ms	Tc=80°C	200	٨
اء			-lc			100	Α
			-lc pulse	1ms		200	
	Collector power dissipation		Pc	1 device		335	W
Junction temperature		Tj			175		
Operating junciton temperature (under switching conditions)		Tjop			150	°C	
Case temperature		Tc			125		
Storage temperature		Tstg			-40 to +125		
Iso	lation voltage	between terminal and copper base (*1) between thermistor and others (*2)	V <sub>iso</sub>	AC : 1min.		2500	VAC
Sc	rew torque	Mounting (*3)	-	M5		3.5	N m

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable value : 2.5-3.5 Nm (M5)

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# ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

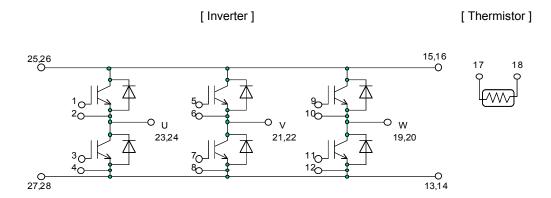
Items		Symbols	Conditions		Characteristics			Units
		Symbols			min.	typ.	max.	Ullits
	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 600V		-	-	1.0	mA
	Gate-Emitter leakage current	Iges	V <sub>GE</sub> = 0V, V <sub>GE</sub> = ±20V		-	-	200	nA
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 100mA		6.2	6.7	7.2	V
	Collector-Emitter saturation voltage	V <sub>CE (sat)</sub> (terminal)	V <sub>GE</sub> = 15V I <sub>C</sub> = 100A	Tj=25°C	-	2.05	2.50	V
				Tj=125°C	-	2.35	-	
				Tj=150°C	-	2.55	-	
		V <sub>CE (sat)</sub> (chip)	V <sub>GE</sub> = 15V I <sub>C</sub> = 100A	Tj=25°C	-	1.60	2.05	
				Tj=125°C	-	1.90	-	
				Tj=150°C	-	2.10	-	
	Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	6.4	-	nF
re	Turn-on time	ton	V <sub>cc</sub> = 300V I <sub>c</sub> = 100A V <sub>GE</sub> = +15 / -15V R <sub>G</sub> = 13Ω		-	0.39	1.20	μs
Inverter		tr			-	0.09	0.60	
		tr (i)			-	0.03	-	
	T 255 L'	toff			-	0.53	1.00	
	Turn-off time	tf			-	0.06	0.30	
		V <sub>F</sub> (terminal)	I <sub>F</sub> = 100A	Tj=25°C	-	2.05	2.50	V
	Forward on voltage			Tj=125°C	-	1.95	-	
		(terrillial)		Tj=150°C	-	1.95	-	
			I <sub>F</sub> = 100A	Tj=25°C	-	1.60	2.05	
		V <sub>F</sub> (chip)		Tj=125°C	-	1.50	-	
				Tj=150°C	-	1.47	-	
	Reverse recovery time	trr	I <sub>F</sub> = ±20		-	-	0.35	μs
tor	D. elektrone	R	T = 25°C		-	5000	-	Ω
Thermistor	Resistance		T = 100°C		465	495	520	
The	B value	В	T = 25 / 50°C		3305	3375	3450	K

### ● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Ullits
harmal registance (Adayisa)	Rth(j-c)	Inverter IGBT	-	-	0.45	°C/W
Thermal resistance (1device)		Inverter FWD	-	-	0.80	
Contact thermal resistance (1device) (*4)	Rth(c-f)	with Thermal Compound	-	0.05	-	

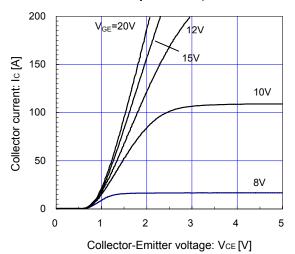
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

# **■** Equivalent Circuit Schematic

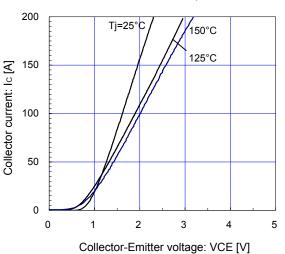


#### ■ Characteristics (Representative)

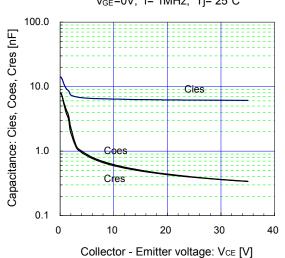
 $\label{eq:continuous} \begin{tabular}{ll} \end{tabular} Inverter \cite{Matter} \cite$ 



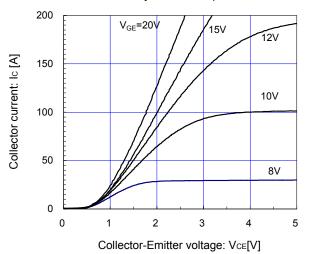
 $[Inverter] \\ Collector current vs. Collector-Emitter voltage (typ.) \\ V_{GE} = 15V / chip$ 



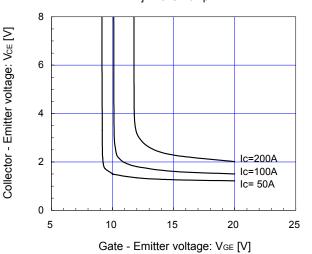
 $[Inverter\ ]$  Capacitance vs. Collector-Emitter voltage (typ.)  $V_{GE} = 0V, \ f = 1MHz, \ Tj = 25^{\circ}C$ 



[ Inverter ] Collector current vs. Collector-Emitter voltage (typ.)  $Tj = 150 ^{\circ} C \ / \ chip$ 



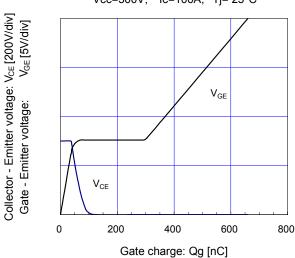
 $\label{eq:continuous} \begin{tabular}{ll} \b$ 



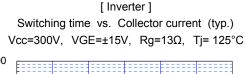
[ Inverter ]

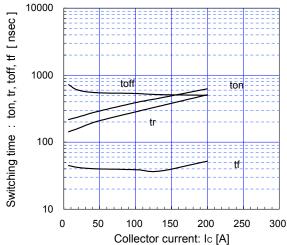
Dynamic gate charge (typ.)

Vcc=300V, Ic=100A, Tj= 25°C

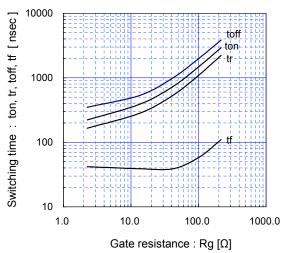


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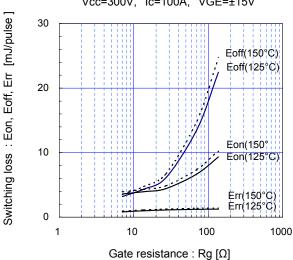




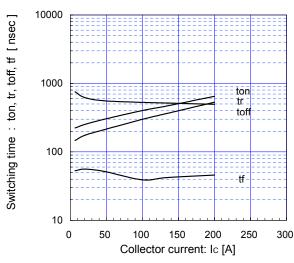
[Inverter]
Switching time vs. gate resistance (typ.)
Vcc=300V, Ic=100A, VGE=±15V, Tj= 125°C



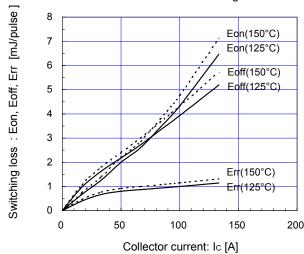
[ Inverter ]
Switching loss vs. gate resistance (typ.)
Vcc=300V, Ic=100A, VGE=±15V



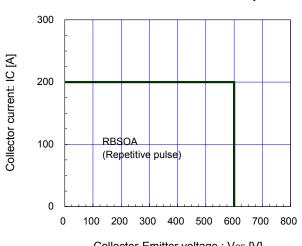
[ Inverter ] Switching time vs. Collector current (typ.) Vcc=300V, VGE= $\pm$ 15V, Rg=13 $\Omega$ , Tj= 150°C



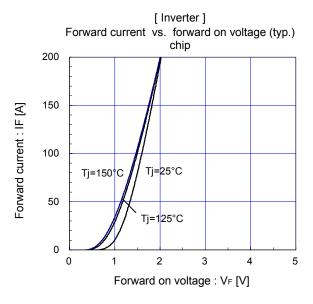
 $[Inverter] \\ Switching loss vs. Collector current (typ.) \\ Vcc=300V, VGE=\pm15V, Rg=13\Omega$ 

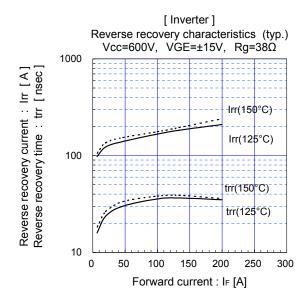


[ Inverter ] Reverse bias safe operating area (max.) +VGE=15V,-VGE <= 15V, RG >=  $13\Omega$ , Tj <=  $125^{\circ}$ C



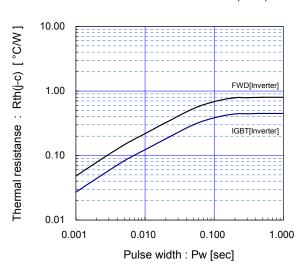
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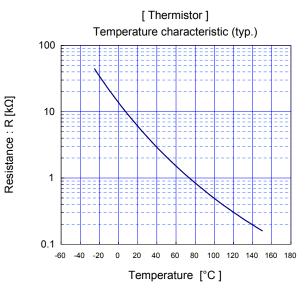




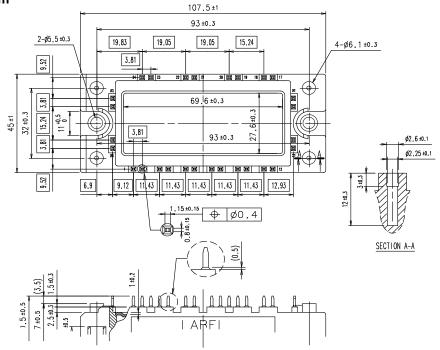
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Transient thermal resistance (max.)





### ■ Outline Drawings, mm



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