



# FCP20N60 / FCPF20N60 600V N-Channel MOSFET

# Features

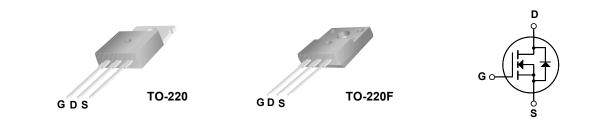
- 650V @T<sub>J</sub> = 150°C
- Typ. R<sub>DS(on)</sub> = 0.15Ω
- Ultra low gate charge (typ. Q<sub>g</sub> = 75nC)
- Low effective output capacitance (typ. C<sub>oss</sub>.eff = 165pF)
- 100% avalanche tested
- RoHS Compliant



# Description

SuperFET<sup>TM</sup> is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



# **Absolute Maximum Ratings**

Symbol	Parameter		FCP20N60	FCPF20N60	Unit	
V <sub>DSS</sub>	Drain-Source Voltage		600		V	
ID	Drain Current	- Continuous (T <sub>C</sub> = 25°C) - Continuous (T <sub>C</sub> = 100°C)		20 12.5	20* 12.5*	A A
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	60	60*	А
V <sub>GSS</sub>	Gate-Source voltage		± 30		V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy		(Note 2)	690		mJ
I <sub>AR</sub>	Avalanche Current		(Note 1)	20		А
E <sub>AR</sub>	Repetitive Avalanche Energy		(Note 1)	20.8		mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.5		V/ns
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C) - Derate above 25°C		208 1.67	39 0.3	W W/°C
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150		°C	
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		е,	300		°C

\*Drain current limited by maximum junction temperature

### **Thermal Characteristics**

Symbol	Parameter	FCP20N60	FCPF20N60	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case	0.6	3.2	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W

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# Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCP20N60	FCP20N60	TO-220	-	-	50
FCPF20N60	FCPF20N60	TO-220F	-	-	50

# Electrical Characteristics T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Off Charac	teristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ = 0V, I <sub>D</sub> = 250 $\mu$ A, T <sub>J</sub> = 25°C	600			V
		$V_{GS}$ = 0V, I <sub>D</sub> = 250 $\mu$ A, T <sub>J</sub> = 150°C		650		V
$\Delta BV_{DSS}$ / $\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C		0.6		V/°C
BV <sub>DS</sub>	Drain-Source Avalanche Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 20A		700		V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 600V, V_{GS} = 0V$ $V_{DS} = 480V, T_{C} = 125^{\circ}C$			1 10	μΑ μΑ
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30V, V <sub>DS</sub> = 0V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V			-100	nA
On Charac	teristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A		0.15	0.19	Ω
<b>g</b> <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 40V, I <sub>D</sub> = 10A (Note 4)		17		S
Dynamic C	haracteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS}$ = 25V, $V_{GS}$ = 0V,		2370	3080	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0MHz		1280	1665	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			95		pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 480V, V <sub>GS</sub> = 0V, f = 1.0MHz		65	85	pF
C <sub>oss</sub> eff.	Effective Output Capacitance	$V_{DS}$ = 0V to 400V, $V_{GS}$ = 0V		165		pF
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 300V, I <sub>D</sub> = 20A		62	135	ns
t <sub>r</sub>	Turn-On Rise Time	$R_{G} = 25\Omega$		140	290	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			230	470	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4, 5)		65	140	ns
Qg	Total Gate Charge	V <sub>DS</sub> = 480V, I <sub>D</sub> = 20A		75	98	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10V		13.5	18	nC
Q <sub>gd</sub>	Gate-Drain Charge	(Note 4, 5)		36		nC
Drain-Sour	rce Diode Characteristics and Maximur	n Ratings				
I <sub>S</sub>	Maximum Continuous Drain-Source Dio	de Forward Current			20	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current				60	А
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A			1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A		530		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$ (Note 4)		10.5		μC

Notes:

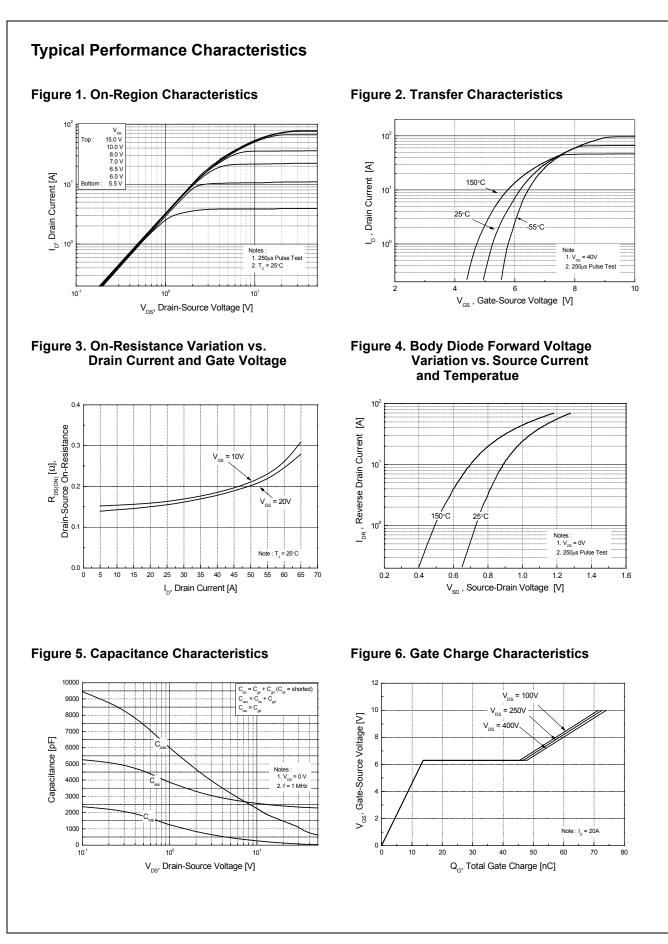
1. Repetitive Rating: Pulse width limited by maximum junction temperature

2.  $I_{AS}$  = 10A,  $V_{DD}$  = 50V,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}C$ 

3. I\_{SD}  $\leq$  20A, di/dt  $\leq$  200A/µs, V\_{DD}  $\leq$  BV\_{DSS}, Starting T\_J = 25°C

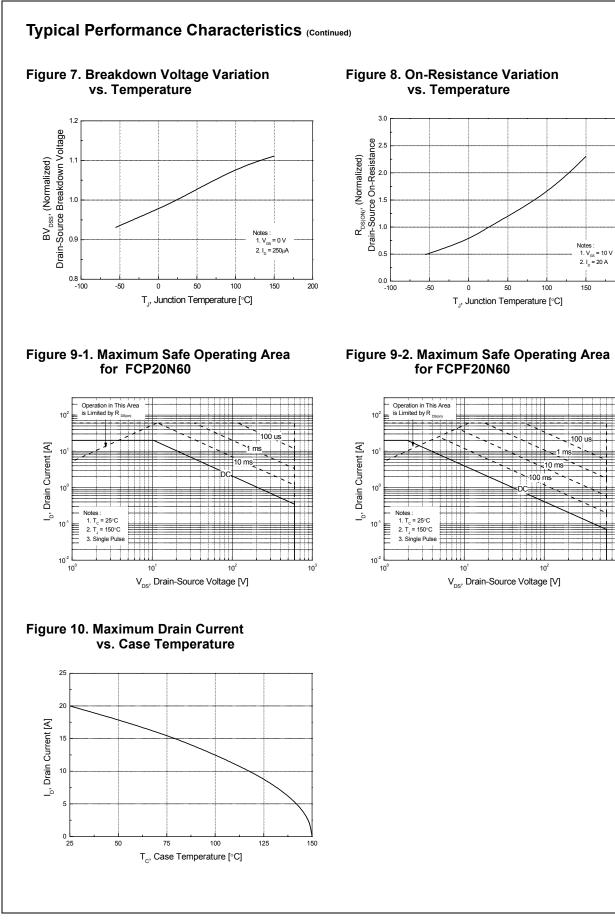
4. Pulse Test: Pulse width  $\leq 300 \mu s, \, \text{Duty Cycle} \leq 2\%$ 

5. Essentially Independent of Operating Temperature Typical Characteristics



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# FCP20N60 / FCPF20N60 600V N-Channel MOSFET

# Typical Performance Characteristics (Continued)



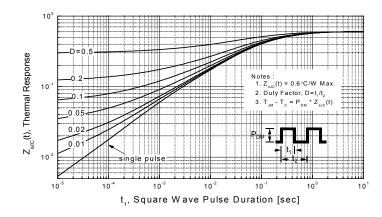
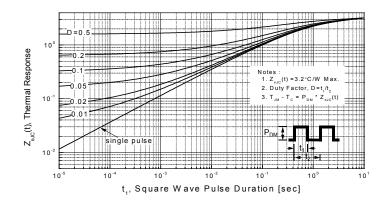
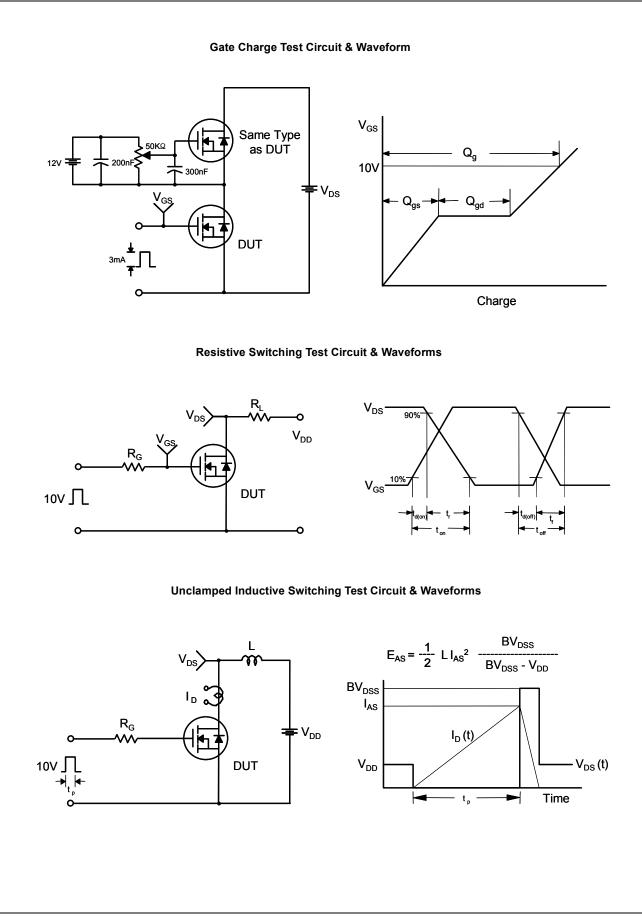
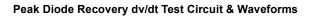


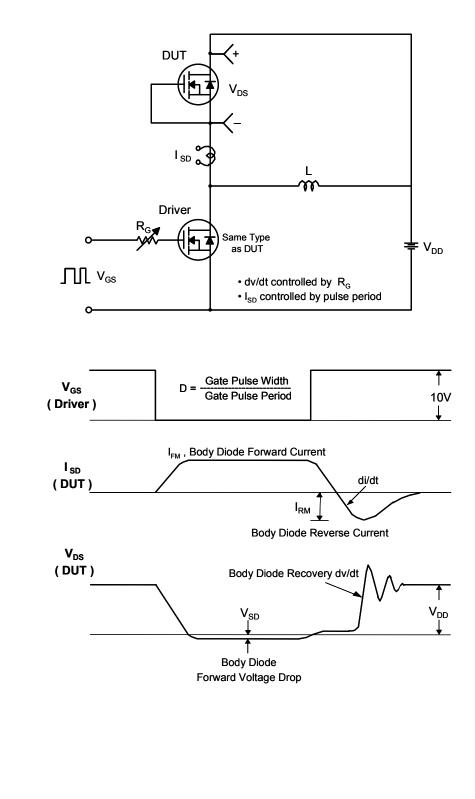
Figure 11-2. Transient Thermal Response Curve for FCPF20N60

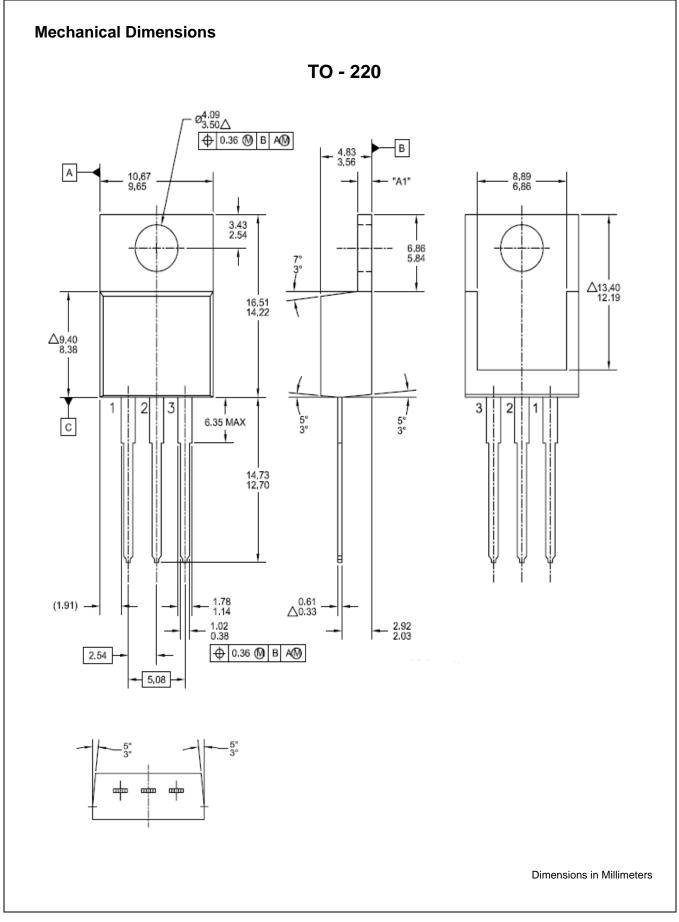


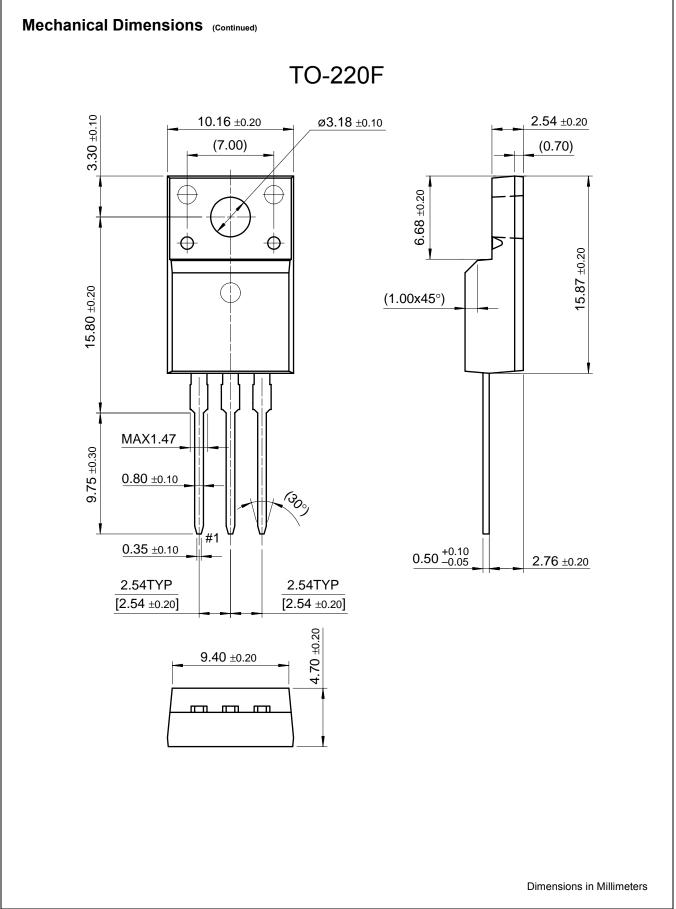


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